

(File no.- 27(3)/2013-IPHW )  
**Government of India**  
**Department of Electronics & Information Technology (DeitY)**

Date : 26-04-2013

**ORDER**

**Subject: Constitution of various Technical Evaluation Committees for evaluation of proposals under MSIPS.**

Reference:

1. MSIPS Policy Gazette Notification No. 175 dated 27-07-2012
2. MSIPS Guidelines dated 07-10-2012 (uploaded on DeitY website)

The Modified Special Incentive Package Scheme ( M-SIPS) to offset disability and attract investment in Electronics System Design and Manufacturing(ESDM) in India was notified vide Notification no. 175 dated 27<sup>th</sup> July 2012 in Part-I , Section 1 of the Gazette of India (Extraordinary). As per para 6.2 of the notification, for effective functioning of M-SIPS a set of guidelines have been drawn and issued by the Department on dated 07-11-2012. [ File no. 27(3)/2012- IPHW]

2. In furtherance of para 8 of the above cited guidelines, **Technical Evaluation Committee (TEC)** for All Electronic products ( IT/ telecom / Nano- electronics / Industrial Electronics/ Consumer Electronics/ Strategic/ Electromechanical Components/ Passive Components/ Consumable & Accessories/ Power Supplies) is constituted. The TEC Member details are as given below:

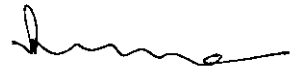
**TEC (All Electronic Products)**

1. Dr. Debashis Dutta., Scientist G, DeitY- Chairman
2. Shri R. K. Pathak, DDG (Investment Promotion), DoT
3. Shri Rajesh Raizada, VP, HCL Infosystems
4. Shri Prakash Katama, Factory Director, Nokia Chennai
5. Prof. A. R. Harish, Department of Electrical Engineering, IIT Kanpur
6. Prof. S.P. Das, Department of Electrical Engineering, IIT Kanpur
7. Shri S K Marwaha, Scientist E, DeitY

3. The Terms of Reference (TOR) of the Technical Evaluation Committee may be same as approved at N/5 which is as given below:

- i) **Name of committee:** Technical Evaluation Committee (All Electronic products IT/ Telecom/ Nano Electronics/ Industrial Electronics/ Consumer Electronics/Strategic Electronics/ Electromechanical components/ Passive Component/ Consumable & Accessories/ Power Supplies).
- ii) **Scope:** The committee will provide recommendation regarding the technology proposed by an applicant and whether the said technology is 'State- of –the –art 'or not as per para 8 of M-SIPS Guidelines.

- iii) **Deliverables:** The committee will provide it's advise on the submissions with respect to the scope as defined above, made to the committee by the DeitY .
- iv) **Mode of Operation :**
- a) The normal mode of operation will be decision by way of circulation. The submissions may be circulated to all the committee members. The members may provide their views via email and the decision will be taken by consensus. However, whenever necessary a meeting may be convened with the approval of the Chairman.
- b) The committee may seek additional technical information related to product or technology proposed.
- v) **Co-opting:** DeitY may nominate other experts as necessary to the Technical Evaluation Committee.
- vi) **TA/DA:** TA/DA for members for attending of meetings will be borne by the Department of Electronics & IT as per rules.
- vii) **Conflict of Interest:** In the case of conflict of interest, it will be the responsibility of the members to inform the Chairman and withdraw himself/herself from the committee for that particular issue/case.



(Dr. Ajay Kumar)  
Joint Secretary  
Tel: 2436016

To:

1. All Committee Members

Copy to:

1. All ministries/ Departments of Govt. of India
2. Cabinet Secretariat
3. PMO
4. Planning Commission
5. Comptroller and Auditor General of India
6. JS & FA, DeitY
7. DeitY website